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TR2017-144 October 2017

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European Microwave Conference

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A Concurrent Triple-Band Digital Transmitter Using Feedforward Noise Cancellation for Delta-Sigma Modulation

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Abstract—A concurrent triple-band digital transmitter architecture with relaxed RF output filter requirement is presented in this paper. With non-contiguous inter-band carrier aggregation, all digital transmitters based on delta-sigma modulation and pulse-width modulation have suffered from out-of-band noise and spurious tones, requiring extremely demanding RF output filter design. We demonstrate a feedforward noise cancellation technique in order to suppress the out-of-band quantization noise of concurrent triple-band delta-sigma modulation for the first time. An experimental prototype based on an asymmetric RF power combiner and a 5-bit 7-GS/s DAC for noise cancellation realizes concurrent triple-band transmission of LTE Advanced signals, which consist of 710 MHz, 1750 MHz, and 2510 MHz bands with 30-MHz aggregated total bandwidth. The prototype achieves better than 42-dB spurious-free dynamic range (SFDR) and -47-dBc adjacent channel power ratio (ACPR), enabled by up to 12-dB out-of-band noise suppression.

Keywords—Digital transmitter, RF filter, feedforward noise cancellation, non-contiguous inter-band carrier aggregation, concurrent multi-band delta-sigma modulation.

I. INTRODUCTION

Advanced mobile communication technologies such as LTE-Advanced are utilizing all available spectrum in order to increase the data rate and the number of simultaneous users in service. Towards this trend of efficient spectrum usage to enhance network capacity, concurrent multi-band (CMB) transmission [1]-[4] is of particular interests, such as non-contiguous inter-band carrier aggregation in LTE Advanced technology. The need of a low-cost implementation with high performance in a small form-factor is driving the recent development of digital-intensive architecture for CMB transmitters.

Advanced digital transmitters (Fig. 1) using highly efficient digital power amplifiers (PAs) [18], [19] are largely based on either RF pulse-width modulation (PWM) [6], [7], [9], delta-sigma modulation (DSM) [5], [11]-[15], or a combination of PWM and DSM [3], [10]. The CMB digital transmitters based on RF PWM typically suffer from spurious tones while the CMB transmitters based DSM commonly show high out-of-band noise. To address these design challenges, digital-intensive transmitters with advanced signal processing as well as analog assistance have been recently introduced. For example, digital predistortion using a look-up table [8], and analog filtering [9], [12] have been applied to digital transmitters based on RF PWM to suppress undesirable spurious tones. Different techniques such as using replica DSMs [4], time-interleaved multi-DSMs [13], and band pass noise cancellation [16] have been reported in an attempt to reduce the out-of-band quantization noise of digital transmitters based on DSM. Nev-

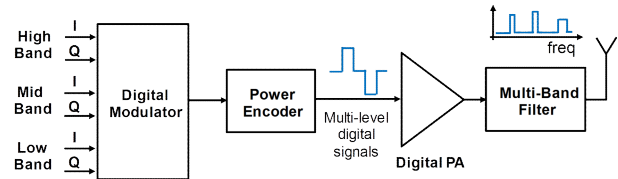


Fig. 1. General concurrent triple-band digital transmitter [3].

ertheless, although these techniques suppress the spurious tones and out-of-band noise, the suppression level of these previous works is not yet sufficient. As a result, the design requirements on the multi-band RF output filter [3] are still very challenging, which not only introduce a large insertion loss and but also constrain flexibility with band selection.

This paper introduces a new wideband out-of-band quantization noise cancellation technique for digital-intensive transmitters based on multi-level CMB-DSM. Although the resolution of the present high-efficiency digital power amplifiers in GaN HEMT technologies stays no more than 3-4 bits [22], [23], it is expected that advanced GaN HEMT process technologies such as [20] will allow a higher resolution (5-6 bits) GaN digital PA such as one corresponding to 6-bit digital PAs in a silicon technology [21]. Even the future GaN digital PAs with 5-6-bit resolution, however, will not satisfy the demanding spectral mask requirements of 4G LTE/5G with wideband modulation. The role of a wideband feedforward noise cancellation technique is therefore instrumental. To generate a wideband noise cancellation signal with over 3-GHz bandwidth, rather than using replica delta-sigma modulators [4], [10] whose bandwidth is typically limited below 1 GHz, we use a high-efficiency wideband digital PA such as [17], [25] whose peak output power is 6-10 dB lower than the desired peak transmitter output power. The complexity and power consumption of such a digital PA with 4-5-bit resolution are significantly lower compared to very high-resolution (12-14 bits) RF DACs [24], which commonly consume a watt-level power, and the overhead in power consumption and complexity for the noise cancellation is not significant.

II. FEEDFORWARD NOISE CANCELLATION FOR CONCURRENT MULTI-BAND DELTA-SIGMA MODULATION (CMB-DSM)

A. Concurrent Triple-Band Delta-Sigma Modulation

Fig. 2 shows a concurrent multi-band delta-sigma modulation (CMB-DSM) for triple-band transmission, which has three inputs each for different frequency bands but produces a single multi-level output to drive a multi-level Class-S or Class-D

*This work was done while SungWon Chung was an intern with MERL.

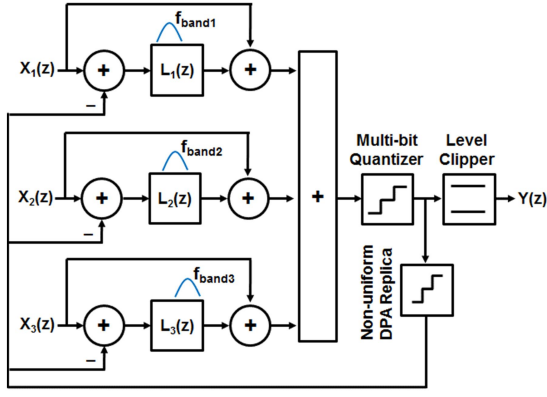


Fig. 2. Concurrent multi-band delta-sigma modulator (CMB-DSM) for triple-band transmission.

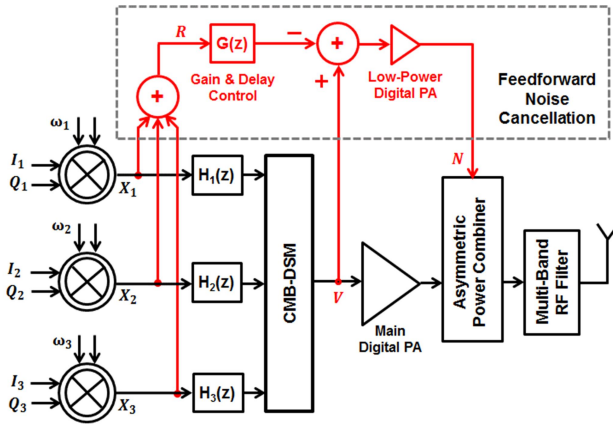


Fig. 3. Feedforward noise cancellation for triple-band transmission using concurrent multi-band delta-sigma modulation (CMB-DSM).

PA. Each of the digital loop filters $L_n(z)$ have three pole-zero pairs that are tuned to resonate at each frequency band. The noise transfer function, which can be shown as

$$NTF(z) = \left(1 + \sum_{i=1}^M L_i(z) \right)^{-1}, \quad (1)$$

therefore has a null at each frequency band. To achieve a higher efficiency by sacrificing modulation accuracy to an acceptable level, an output clipper [3] is employed, which reduces the number output levels in the CMB-DSM output by clipping out the maximum and the minimum amplitude levels. The main design challenge with CMB-DSM is to maintain the modulator stability when the multiple frequency bands are close to each other and also when the digital loop filters have a higher-order structure.

B. Feedforward Out-of-Band Noise Cancellation

Fig. 3 illustrates the proposed feedforward out-of-band noise cancellation technique for multi-level CMB-DSM. A high-efficiency low-power digital PA generates a wideband noise cancellation signal. The gain and delay control as well as the

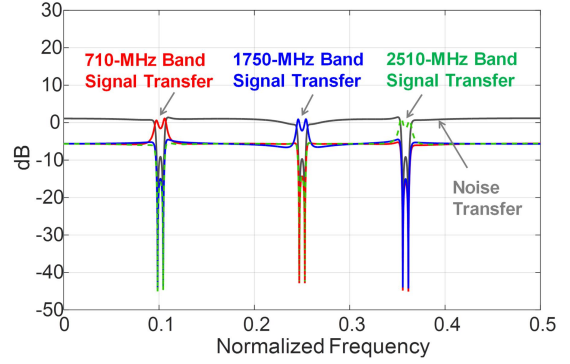


Fig. 4. Simulated noise and signal transfer functions of a CMB-DSM for concurrent triple-band transmission.

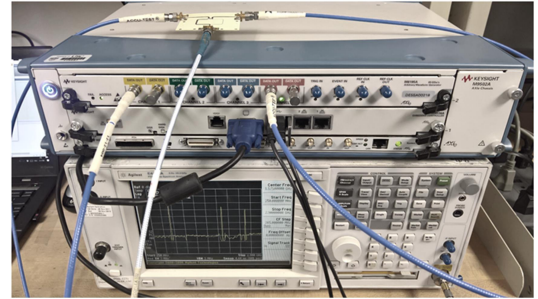
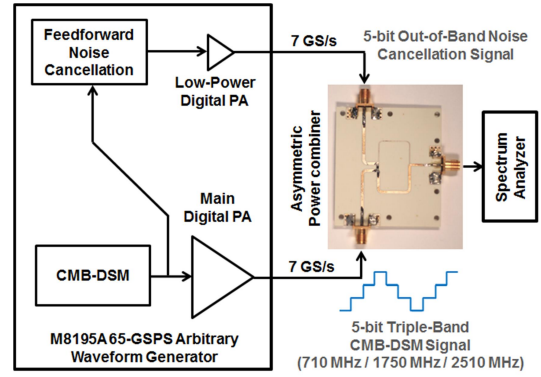


Fig. 5. Measurement setup of feedforward noise cancellation for concurrent triple-band transmission with LTE Advanced signals using 30-MHz aggregated bandwidth.

CMB-DSM predistortion function $H_n(z)$ are adjusted to reduce the output power requirement on the digital PA for noise cancellation. The efficiency η of the overall transmitter is given by

$$\eta = (\eta_1^{-1} + k \eta_2^{-1})^{-1}, \quad (2)$$

where η_1 and η_2 are the efficiencies of the main digital PA and the noise cancellation PA, respectively, and k is the average noise cancellation signal power normalized to the average main digital PA output power.

Since the noise cancellation signals are computed as the difference between the CMB-DSM output and the reference output, the CMB-DSM is designed for stability at the expense

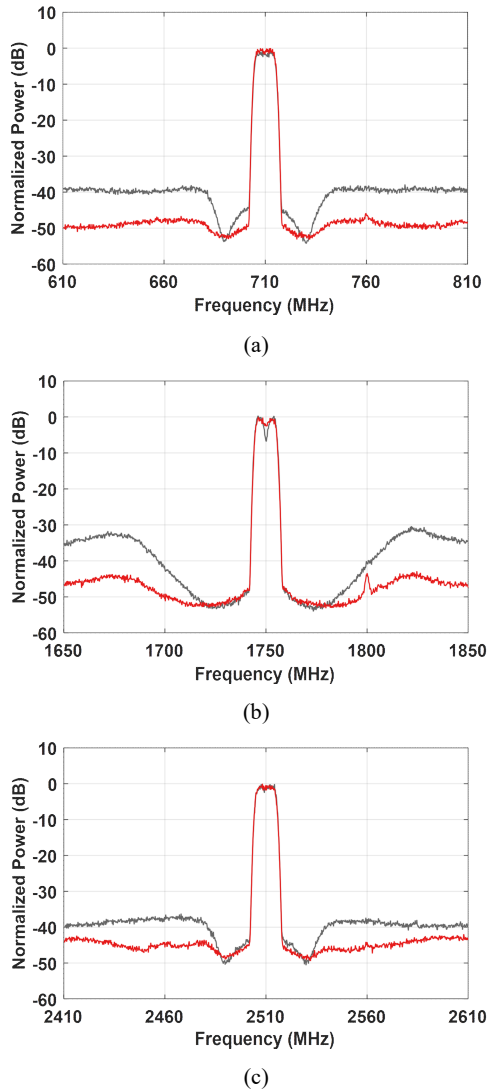


Fig. 6. Measured close-in spectrum of concurrent triple-band LTE-Advanced transmission with 30-MHz aggregated bandwidth: (a) 710 MHz band, (b) 1750 MHz band, (c) 2510 MHz band (dark gray: without noise cancellation, red: with feedforward noise cancellation).

of flat frequency response in the signal band, assuming the noise cancellation signal will provide necessary correction on the frequency response. This design technique resolves the noise peaking problem of a concurrent multi-band digital transmitter [4].

III. IMPLEMENTATION

The digital loop-filters of CMB-DSM with 5-bit output resolution are designed for the simultaneous LTE Advanced transmission of 710 MHz, 1750 MHz, and 2510 MHz band signals with overall 30-MHz aggregated bandwidth. Fig. 4 shows the signal transfer function and the noise transfer function with the three digital loop filters in the CMB-DSM. For

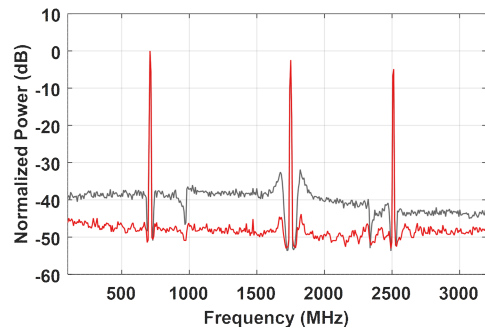


Fig. 7. Measured wideband spectrum of the concurrent triple-band LTE transmission (dark gray: without noise cancellation, red: with feedforward noise cancellation).

TABLE I
COMPARISON OF CONCURRENT MULTI-BAND DIGITAL TRANSMITTERS.

Ref.	[2] 2013	[3] 2015	[14] 2017	This work
# of Concurrent Carriers	2	2	2	3
Modulation Resolution	1 bit	1.5 bit	1 bit	5 bits
Carrier Frequency	800 MHz / 1500 MHz	856 MHz 1450 MHz	2620 MHz 5240 MHz	710 MHz 1750 MHz 2510 MHz
Sampling Frequency	3.9 GHz	25 GHz	24.5 GHz	7 GHz
Transmit Signal	LTE	LTE	LTE / Wi-Fi	LTE
PAPR	6.5 dB	11.7 dB	5 dB	11.7 dB
Channel Bandwidth	5 MHz / 5 MHz	20 MHz / 20 MHz	5 MHz / 20 MHz	10 MHz / 10 MHz / 10 MHz
Aggregated Bandwidth	10 MHz	40 MHz	25 MHz	30 MHz
SFDR	< 5 dB	6 dB	34 dB / 30 dB	46 dB / 43 dB / 42 dB
ACPR	-51 dBc / -44 dBc	-46 dBc / -43 dBc	-48 dBc / -35 dBc	-50 dBc / -48 dBc / -47 dBc

the modulator stability, the order of the digital loop-filters is limited to no more than 6.

Fig. 5 shows an experimental setup on current triple-band transmission where an asymmetric power combiner, which is optimized for an uneven input power ratio [4], performs the noise cancellation. Agilent M8195A 65-GS/s arbitrary wave form generator (AWG) is configured to have a sampling rate of 7 GS/s to emulate a fourth-order concurrent triple-band CMB-DSM. 5-bit 7-GS/s noise cancellation low-power digital PA is emulated by a different output channel of the AWG. The noise cancellation signal power is 8.7 dB below the main digital power amplifier output power. If the setup includes a main digital PA and a noise cancellation PA both with 50% efficiency, the overall transmitter efficiency can be obtained from (2) as 44%. Considering the insertion loss of triple-band RF output filters (typically 2 dB or higher, which will degrade the transmitter efficiency from 50% to below 31%), the proposed noise

cancellation technique provides more than 13% higher efficiency by removing the need of a multi-band RF output filter.

IV. MEASURED RESULTS

Fig. 6 shows the measured wideband spectrum on the concurrent triple-band transmission of LTE Advanced signals where feedforward noise cancellation improves the noise floor up to 12 dB. The adjacent channel power ratio (ACPR) for 700-MHz, 1750-MHz, and 2510-MHz band is -50 dBc, -48 dBc, and -47 dBc, respectively. Spurious free dynamic range (SFDR) for each band, which determines the filter design complexity, is 46 dB, 43 dB, and 42 dB, respectively. Fig. 6(b) exhibits the improvement of in-band signal frequency response as discussed in Section II-B concerning modulator stability.

Fig. 7 shows the wideband spectrum from 200 MHz to 3200 MHz, showing that the noise floor improvement by the feedforward noise cancellation varies over frequencies. This incomplete noise cancellation can be explained by the delay mismatch as well as gain/phase response mismatch between the main amplifier path and the noise-cancelling amplifier path.

Table I summarizes the performance of the prototype in comparison with previous works on concurrent multi-band digital transmitters. The multi-bit CMB-DSM of this work allows a relatively low sampling frequency for superior SFDR and a high peak-to-average power ratio (PAPR) of 11.7 dB.

V. CONCLUSION

For the first time, a concurrent triple-band transmitter based on digital architecture using multi-band delta-sigma modulation is demonstrated towards a low-cost implementation of advanced communication transmitters with a high-efficiency and a small form-factor. Wideband feedforward noise cancellation on the out-of-band noise of delta-sigma modulation can significantly relax the multi-band RF output filter design for concurrent multi-band digital transmitters. If the noise cancellation is enough to remove the RF output filters, digital transmitters can offer a greater flexibility in carrier frequency reconfiguration. Since the proposed noise cancellation technique does not depend on a particular digital modulation technique, it can be applied to general multi-level digital modulators.

ACKNOWLEDGMENT

The authors would like to thank T. Koike-Akino, P. Orlik, and J. Shao at Mitsubishi Electric for their technical support.

REFERENCES

- [1] N. V. Silva, A. S. R. Oliveira *et al.*, "A novel all-digital multichannel multimode RF transmitter using delta-sigma modulation," *IEEE Microw. Wireless Comp. Lett.*, vol. 22, no. 3, pp. 156-158, Mar. 2012.
- [2] T. Maehata, K. Totani, S. Kameda, and N. Suematsu, "Concurrent dual-band 1-bit digital transmitter using band-pass delta-sigma modulator," in *Proc. IEEE European Microw. Conf.*, Oct. 2013, pp. 1523-1526.
- [3] S. Chung, R. Ma, S. Shinjo, H. Nakamizo, K. Parsons, and K. H. Teo, "Concurrent multiband digital outphasing transmitter architecture using multidimensional power coding," *IEEE Trans. Microw. Theory Techn.*, vol. 63, no. 2, pp. 598-613, Feb. 2015.
- [4] S. Chung, R. Ma, S. Shinjo, and K. H. Teo, "Inter-band carrier aggregation digital transmitter architecture with concurrent multi-band delta-sigma modulation using out-of-band noise cancellation," in *Proc. IEEE MTT-S Int. Microw. Symp. Dig.*, May 2015, pp. 1-4.

- [5] J. Choi *et al.*, "A $\Delta\Sigma$ -digitized polar RF transmitter," *IEEE Trans. Microw. Theory Techn.*, vol. 55, no. 12, pp. 2679-2690, Dec. 2007.
- [6] Q. Zhu, R. Ma *et al.*, "A 5-level discrete-time power encoder with measured coding efficiency of 70% for 20-MHz LTE digital transmitter," in *Proc. IEEE MTT-S Int. Microw. Symp. Dig.*, 2014, pp. 1-4.
- [7] D. Seebacher *et al.*, "Reduction of aliasing effects of RF PWM modulated signals by cross point estimation," *IEEE Trans. Circ. Syst. I: Reg. Papers*, vol. 61, no. 11, pp. 3184-3192, Nov. 2014.
- [8] D. Seebacher *et al.*, "Predistortion of digital RF PWM signals considering conditional memory," *IEEE Trans. Circuits Syst. I: Reg. Papers*, vol. 62, no. 9, pp. 2342-2350, Sept. 2015.
- [9] K. Hausmair, S. Chi, P. Singerl, and C. Vogel, "Aliasing-free digital pulse-width modulation for burst-mode RF transmitters," *IEEE Trans. Circuits Syst. I: Reg. Papers*, vol. 60, no. 2, pp. 415-427, Feb. 2013.
- [10] R. Hezar, L. Ding, J. Hur, and B. Haroun, "A 23 dBm fully digital transmitter using $\Delta\Sigma$ and pulse-width modulation for LTE and WLAN application in 45nm CMOS," in *IEEE Radio Freq. Integ. Circ. Symp. Dig. Papers*, June 2014, pp. 217-220.
- [11] D. C. Dinis, R. F. Cordeiro, F. M. Barradas, A. S. R. Oliveira, and José Vieira, "Agile single- and dual-band all-digital transmitter based on a precompensated tunable delta-sigma modulator," *IEEE Trans. Microw. Theory Techn.*, vol. 64, no. 12, pp. 4720-4730, Dec. 2016.
- [12] R. F. Cordeiro, A. S. Oliveira, and J. N. Vieira, "All-digital transmitter with a mixed-domain combination filter," *IEEE Trans. Circ. Syst. II: Exp. Briefs*, vol. 63, no. 1, pp. 4-8, 2016.
- [13] R. F. Cordeiro, A. S. R. Oliveira, J. N. Vieira, and T. Silva, "Wideband all-digital transmitter based on multicore DSM," in *IEEE MTT-S Int. Microw. Symp. Dig.*, pp. 1-3, 2016.
- [14] D. Markert, X. Yu, H. Heimpel, and G. Fischer, "An all-digital, single-bit RF transmitter for massive MIMO," *IEEE Trans. Circ. Syst. I: Reg. Papers*, vol. 64, no. 3, pp. 696-704, Dec. 2016.
- [15] M. Tanio, S. Hori, M. Hayakawa, N. Tawa *et al.*, "A linear and efficient 1-bit digital transmitter with envelope delta-sigma modulation for 700MHz LTE," in *IEEE MTT-S Int. Microw. Symp. Dig.*, pp. 1-2, 2014.
- [16] U. Gustavsson, T. Eriksson, H. M. Nemati, P. Saad, P. Singerl, and C. Fager, "An RF carrier bursting system using partial quantization noise cancellation," *IEEE Trans. Circ. Syst. I*, vol. 59, pp. 515-528, Mar. 2012.
- [17] S. Chung, R. Ma, and K. H. Teo, "Design considerations on wideband envelope termination for high efficiency RF power amplifiers," *Electronics Letters*, vol. 52, no. 6, pp. 460-462, 2016.
- [18] R. Ma, "Recent advances in power encoding and GaN switching technologies for digital transmitters," in *Proc. IEEE Asia-Pacific Microw. Conf.*, Dec. 2015, pp. 1-3.
- [19] A. Wentzel, S. Chevtchenko, P. Kurpas, and W. Heinrich, "A flexible GaN MMIC enabling digital power amplifiers for the future wireless infrastructure," in *IEEE MTT-S Int. Microw. Symp. Dig.*, pp. 1-4, 2015.
- [20] J. W. Chung, J.-K. Lee, E. L. Piner, and T. Palacios, "Seamless on-wafer integration of Si (100) MOSFETs and GaN HEMTs," *IEEE Electron Device Lett.*, vol. 30, no. 10, pp. 1015-1017, Aug. 2009.
- [21] A. Kavousian *et al.*, "A digitally modulated polar CMOS power amplifier with a 20-MHz channel bandwidth," *IEEE J. Solid-State Circuits*, vol. 43, no. 10, pp. 2251-2258, Oct. 2008.
- [22] M. Watanabe and T. LaRocca, "A 3-bit, 2-watt, digital-analog gallium nitride power amplifier for 64-QAM bandwidth efficient modulation with 25% power savings," in *IEEE Radio Freq. Integ. Circ. Symp. Dig. Papers*, June 2012, pp. 1-4.
- [23] M. Watanabe, R. Snyder, and T. LaRocca, "Simultaneous linearity and efficiency enhancement of a digitally-assisted GaN power amplifier for 64-QAM," in *IEEE Radio Freq. Integ. Circ. Symp. Dig. Papers*, June 2012, June 2013, pp. 1-4.
- [24] V. Ravinuthula *et al.*, "A 14-bit 8.9GS/s RF DAC in 40nm CMOS achieving >71dBc LTE ACPR at 2.9GHz," in *IEEE Symp. VLSI Circuits Dig. Tech. Papers*, June 2016, pp. 1-2.
- [25] K. Nakatani, S. Shinjo, S. Miwa, R. Ma, and K. Yamanaka, "3.0-3.6 GHz wideband, over 46% average efficiency GaN Doherty power amplifier with frequency dependency compensating circuits," in *Proc. IEEE Radio Wireless Week*, Jan. 2017, pp. 1-3.